

Draft Minutes for PL22.3 (J3) Meeting 217

Document No. PL22.3-2018-00285-draft-minutes

Document
Date: 10/19/2018

Reply To: [Dan Nagle](#)

Subject: Draft Minutes of INCITS PL22.3 Working group for Fortran
October 15-19, 2018 (8:00AM – 5:00PM Pacific), In-person

1. Administrative

1.1 Call to Order and Chairman's Remarks October 15, 2018 8:00AM
by the chairman, Dan Nagle. Lorri Menard acted as Recording Secretary.

1.2 INCITS Patent Policy and Antitrust
Reference: <http://www.incits.org/standards-information/legal-info>

1.3 Membership Report

1.3.1 Member organizations:
(Note: Voting members are "YES;" non-voting but attending are "yes")

Company	First Name	Participant Class	Membership Class	In Attendance
Corbett	Robert Corbett	Principal	Voting	YES
Cray Inc	Bill Long	Principal	Voting	YES
	Andrew Gontarek	Alternate	Voting	
IBM Corporation	Daniel Chen	Principal	Voting	YES
	Kelvin Li	Alternate	Voting	
	Rafik Zurob	Alternate	Voting	
Intel Corporation	Raghu Maddhipatla	Alternate	Voting	
	Divya Mangudi	Alternate	Voting	
	Lorri Menard	Principal	Voting	YES
	Jon Steidel	Alternate	Voting	yes
Jet Propulsion Laboratory	Van Snyder	Principal	Voting	YES
Kernelyze LLC	Thomas Knox	Principal	Voting	
	Anton Shterenlikht	Alternate	Voting	
Lawrence Berkeley National Laboratory	Bryce Adelstein-Lelbach	Principal	Voting	
	Brian Friesen	Alternate	Voting	YES
Lionel	Steve Lionel	Principal	Voting	YES
	Malcolm Cohen	Alternate	Voting	yes
	Vipul Parekh	Alternate	Voting	yes

NASA	Thomas Clune	Principal	Voting	YES
	Henry Jin	Alternate	Voting	
National Center for Atmospheric Research (NCAR)	Dan Nagle	Principal	Voting	YES
	John Reid	Alternate	Voting	
	John Wallin	Alternate	Voting	
NVidia Corporation	Gary Klimowicz	Principal	Voting	YES
	Mark LeAir	Alternate	Voting	
United States Dept of Energy	Aleksandar Donev	Alternate	Voting	
	Karla Morris	Principal	Voting	
	Damian Rouson	Alternate	Voting	YES
	Craig Rasmussen	Alternate	Voting	yes
Oak Ridge National Labs		Principal	Voting	

** At the beginning of the meeting, Vipul Parekh, was a guest. During the meeting, Vipul was approved as an alternate for Steve Lionel.

- 1.3.2 Member organizations with voting rights:
There are 12 member organizations with voting rights, and one organization that is pending (Oak Ridge National Labs). The pending organization has not yet completed the prerequisite observation meeting.
Eleven of the 12 voting organizations are represented at this meeting.
- 1.3.3 Member organizations gaining voting rights at this meeting:
No organizations have gained voting rights at this meeting
- 1.3.4 Member organizations that have lost voting rights due to lack of attendance:
No organizations have lost voting rights due to attendance
- 1.3.5 Member organizations in attendance jeopardy:
No organizations are in attendance jeopardy
- 1.3.6 Member organizations in ballot jeopardy:
No organizations are in ballot jeopardy

1.4 Approval of Previous Meeting Minutes

Reference:

<https://standards.incits.org/apps/org/workgroup/pl22.3/download.php/98687/pl22.3-2018-00158-001-agenda%20for%20m216.docx>

Motion to approve by Gary Klimowicz, seconded by Tom Clune; passed with unanimous consent

- 1.5 Review of Action Items
There were no action items

1.6 Approval of the Draft Agenda

Reference:

<https://standards.incits.org/apps/org/workgroup/pl22.3/download.php/102203/pl22.3-2018-00252-002-PL22.3-agenda-for-m217.docx>

Motion to approve by Jon Steidel, seconded Steve Lionel, and passed with unanimous consent

1.7 Review of Ad Hoc

2. Agenda

1. Monday, October 15, 2018 8:00 am

1.1 Opening business

D. Nagle

Remarks from the chair

D. Nagle

Dan welcomed us to meeting 217

Dan noted PL22.3's deep sorrow at the passing of long time member, Stan Whitlock.

Dan reiterated that our goal for this meeting is to address the line items on the worklist from meetings 215 and 216. Subgroups are encouraged to get requirements and list use-cases for each of the proposed features.

Adoption of agenda

D. Nagle

Motion made by Steidel/seconded Clune to adopt the agenda, and unanimous consent (UC)

Approval of Meeting 216 minutes

D. Nagle

Motion made by Steidel/ Klimowicz to approve the m216 minutes - UC

INCITS report (if any)

D. Nagle

We need to vote on *ISO/IEC TS 29113:2012 (vers 2), Information technology -- Further interoperability of Fortran with C* and on *ISO/IEC TS 18508:2015, Information technology -- Additional Parallel Features in Fortran* This will be done on Thursday afternoon.

IEEE/754 report (if any)

R. Corbett

This committee is currently chaired by David Hough and the new editor is Mike Cowlshaw.

Their goal was to have the next standard completed by 2018 to avoid reaffirming old standard, and clearly time is tight. There are no major new features, but a number of minor features including augmented arithmetic. Current status: email ballot in August froze the technical content; all changes are supposed to be editorial.

WG23 report (if any)

D. Nagle

There is a new revision out for review. It has changes to the C and Ada annexes. Dan will resume work on the Fortran annex in the coming months.

D. Nagle

MPI Liaison report (if any)

B. Long

From the June, 2018 MPI forum meeting:

The biggest news at the MPI Forum in Austin:

- (1) persistent collectives passed its first vote,
- (2) a limited subset of ULFM might make it into MPI 4.0, and
- (3) the accelerated schedule for MPI 4.0. (March, 2020).

Virtual meetings are already being used to speed up working group discussions, and we discussed an extra in-person meeting each year until MPI 4.0 is published.

The hybrid working group discussed 5 issues:

Clarification of "process" in the MPI standard. This requires reviewing the entire MPI standard and making any necessary changes to any text containing the word "process". Pavan asked if anyone was willing to help with this effort. No one volunteered.

Detecting if there are more than one MPI processes in an OS process.

Support for per-object locks.

User visible endpoints. It was generally agreed that user-visible endpoints aren't a performance win. Most people decided that "wait-and-see" was the best path to take for user-visible endpoints.

Modified/new thread support levels.

The sessions WG still wasn't entirely sure what sessions are, or what we should call them ("instances", maybe?). Nonetheless, the goal was to start writing some of the standard-ese for sessions. We managed to write the prototypes for `MPI_Session_init` and `MPI_Session_finalize`.

Here's the result:

```
MPI_Session_init(  
    INOUT MPI_Flags *flags,  
    IN MPI_Info info,  
    IN MPI_Errhandler errhandler,  
    OUT MPI_Session *session)
```

```
MPI_Session_finalize(INOUT MPI_Session *session)
```

Session initialization is intended to be local, but there was some discussion if this is the right path to take. James Dinan argued that it's more efficient for all ranks to be wired up at the beginning of the job.

MPI_Session_finalize is not collective (although there was some discussion about this). However, it can block waiting for destruction of objects derived from the session handle.

The collectives working group discussed an info key that would force persistent collectives to start in order across the communicator. This could be useful for implementing persistent collectives using hardware support for collectives.

The fault tolerance working group generally agreed that ULFM needs to be broken into four pieces. The first three most people like, but the fourth is controversial.

1) general definitions --> process failure, changes to existing functions

2) failure notification/discovery -->

MPI_ERR_PROC_FAILED, MPI_COMM_FAILURE_ACK/GET_ACKED

3) fault tolerant agreement --> MPI_Comm_agree

4) failure recovery [contentious, need to address composability concerns] -->

MPI_Comm_shrink, MPI_Comm_revoke

The first three parts are likely to make it into MPI 4.0.

MPI 4.0 is expected to be ready by March 2020 (assuming we consistently meet quorum, continue virtual meetings, and maybe add an extra in-person meeting). MPI 4.0 is expected to contain:

- persistent collectives
- large count
- (maybe) MPI_T events
- (maybe) "create from group" functions for sessions
- (maybe) FP16

OpenMP Liaison report (if any)

B. Long

Since last June, the OpenMP language committee released a comment draft and have been primarily in quality control mode for the spec. There are some last minute changes that have gone in:

(1) Fixes for mapping Fortran array sections based off of pointers on device constructs

(2) Some added combined taskloop constructs for usability

(3) Some changes to how declare variant works (the directive now goes on the base function and specifies different variants to be used depending on the context in which the base function is invoked). Removed "implements" clause from declare target, since declare variant now provides that functionality.

(4) OMP_NESTED, omp_set_nested, and omp_get_nested now use the same internal control variable as OMP_MAX_ACTIVE_LEVELS.

Other than that, there was a F2F meeting last week that included some more discussions on future directions.

The final draft for OpenMP 5.0 is currently being prepared for a final vote by the OpenMP ARB, and should be publicly released at SC'18.

UPC/PGAS Liaison report (if any)

B. Friesen

GasNET-ex still getting support from DoE, otherwise little or no new development. There will be a workshop at Supercomputer on this.

OpenACC Liaison report (if any)

G. Klimowicz

Information about OpenACC, including the current standard document, training materials, and upcoming events can be found at <http://www.openacc.org/>.

The current OpenACC 2.6 standard includes support for manual deep copy of data structures to target processors.

There are over 110 applications in production or development using OpenACC, including:

- Gaussian 16
- ANSYS Fluent
- VASP
- MPAS-A
- COSMO
- GAMERA for GPU
- Quantum Espresso

There is one more "hackathons" coming up in 2018, where researchers bring their codes to work alongside with OpenACC experts to accelerate their applications. Event dates are posted at <https://www.openacc.org/events>.

- ORNL GPU Hackathon October 22-26

There is an OpenACC online course being taught beginning October 18th, 2018. There are three classes planned so far. Experience with OpenACC or GPU programming is not assumed.

Date	Topic
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October 18	#1 - OpenACC Basics (1 hour lecture and 30 minutes Q&A)
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October 25	#2 - GPU Programming with OpenACC (1 hour lecture and 30 minutes Q&A)
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November 1	#3 - Optimizing and Best Practices for OpenACC
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(1 hour lecture and 30 minutes Q&A)

You can register at <https://www.openacc.org/events/openacc-online-course-2018>.

OpenACC is working on version 2.7:

Things they really want to get into the spec in the fall:

1. Make it clear that the host can be a device.
2. Listing which fortran intrinsics and math. h functions should be supported.
3. Fortran bindings for API routines.
4. Treat reduction as a data clause (for parallel loop reduction).
5. Clarify that the host data construct used without "use device" means nothing

Two more things they'd really like to get in:

6. Array reductions
7. "acc parallel self" - run this in parallel on the current device

We also understand that at the OpenMP face-to-face meeting this fall that the OpenMP group is planning to create a roadmap for how OpenACC constructs are mapped to OpenMP constructs. This would effectively mean that "#pragma acc" would be treated as a new way to spell "#pragma omp" and dramatically reduce the porting problem for codes that used both.

Flang open source report (if any)

G. Klimowicz

Flang is an open source compiler for Fortran, sponsored by the US Department of Energy (particularly, LLNL, Sandia and LANL).

The goals of the project are to

- Create a new, open source Fortran 2018 compiler with Apache 2.0 licensing,
- that can be used for language and parallelization experimentation,
- that exists as a peer in the LLVM community of languages, like Clang,
- that can rely on LLVM code generation and parallelism support for CPUs and GPUs.

The initial version of Flang is derived from the PGI Fortran compiler, with some proprietary features removed (OpenACC support, inter-procedure analysis). It was published on GitHub in May 2017 at github.com/flang-compiler, and consists of several subprojects (including the Flang driver and compiler itself, and changes to LLVM to support Fortran debug metadata to be upstreamed). The current compiler supports Fortran 2003 and some Fortran 2008 features.

Recent improvements to Flang include:

- Support for SUBMODULE
- Support for DO CONCURRENT
- Support for internal procedure pointers

- Many bug fixes and enhancements from the PGI compiler

Flang is available for Linux on x86-64, OpenPOWER and Arm processors, and is the basis of the Arm commercial Fortran compiler. Members of the community are also working on ports to Mac OS X and packages for OpenBSD and FreeBSD.

There are flang-dev and flang-announce mailing lists you can join for discussion of Flang at <http://lists.flang-compiler.org/> and a Slack channel, <http://flang-compiler.slack.com/> for more interactive communication with the Flang community.

The Flang project team recognizes that the older code base used to seed Flang is not going to meet the long-term goals of the project, and NVIDIA has begun a new project to rewrite the Fortran front-end in C++ to better align with the LLVM and Clang communities and to better leverage the existing tools and techniques from these communities.

This new front-end, which we call F18, is available at <https://github.com/flang-compiler/f18>. All development for F18 is being done on the open source repository, and you can follow the pull request activity from our developers there.

We delivered two webinars introducing F18 to the Flang, Clang and LLVM communities in July. The recording for the second webinar (slightly expanded from the first) can be found at <https://nvmeet.webex.com/nvmeet/ldr.php?RCID=8b42b4ab49dec35519867c7fea8719e0>.

We are currently working on declaration and type processing, expression evaluation for compile-time expressions, and statement semantics (DO and DO CONCURRENT). We now have enough of the infrastructure for parsing, symbol table, error handling and semantic representation that we encourage developers outside NVIDIA to participate.

There is a biweekly half-hour conference call providing status updates on Flang, every other Wednesday at 8:30 AM Pacific time (the next is October 17, 2018). If you are interested in participating in these calls, please let Gary Klimowicz (gklimowicz@nvidia.com) know and he will forward the meeting invitation.

Beginning Treasurer's report

15 Oct 2018 Opening balance	\$ 2682.36
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J. Steidel

Beginning membership report

L. Menard

12 member organizations, one more pending, 11 represented here.	
Total Membership at beginning of Meeting 217	: 12 (plus one pending)
Majority [1 over half]	: 7

Quorum [1/3 of voting members, min 4 present] : 4

Local arrangements

D. Nagle

Comments from members

1.2 Tutorials (if needed)

Not a tutorial per se, but Dan and Malcolm explained what our goal is for this meeting. We have a worklist of topics (18-010) many of which already have explanatory papers, and some are still needed.

There also have been some papers submitted for other features to be considered for F202x.

The goal for this meeting and next is to process all the existing papers, and to update 18-010 with the set of features that J3 proposes to Wg5 for consideration at the next joint meeting in June 2019.

For these purposes, "passing a paper" means that it will go forward for consideration and renovation, not that it is guaranteed to be in the next standard, in the exact form it was described in the paper that passed.

1.3 Subgroup organization

D. Nagle

/JoR: Dan Nagle (head), Gary Klimowicz, Steve Lionel, Lorri Menard

/Data: Malcolm Cohen (head), Tom Clune, Bob Corbett, Damian Rouson, Van Snyder.

/HPC: Bill Long (head), Daniel Chen, Brian Friesen, Jon Steidel.

/INTERP: Malcolm Cohen (head)

There is no /EDIT business scheduled for this meeting.

Recessed to subgroup meetings at 9:00

1.4 Subgroup meetings

1.5 Subgroup reports (4:30 pm)

/JoR

For vote tomorrow: 18-238r1, 18-253r1, 18-266r1, 18-268

No action on:

18-242 "Irregularities (index variables)"

Seemingly syntax-sugar change would have semantic effect.

No value seen.

18-243 "Medium-grain parallelism"

This topic not on work list

18-247 "Supporting rank genericity"

Some features interesting; essentially vector subscripts

18-248 "Supporting rank variability"

No action; no need to add something we can already do

18-249 "Supporting generic programming"

No action; no use cases

/Data

Status:

18-237 "Asynchronous Procedure Execution"

no action because existing parallel tools, posix or OpenMP, do an excellent job.

18-239 "logical short-circuits"

No action on this paper because 13-234 is an improvement on this, and will reappear as a new paper.

18-241 "(DIN) Persistency option for dummy argument attributes"

Subgroup has been studying, no comment yet

18-240r1 "On the semantic options for templates/generics"

Subgroup has spent much time with this, no comment yet

18-254 "real-complex interoperability"

No action on this paper; will be replaced by one with an example of using `c_f_pointer`.

/HPC

Status:

18-246 "Synchronizing variables"

No action; to be considered as part of 18-237

18-264 "Inconsistencies between LOCAL and BLOCK"

There is a 18-264r1 in main folder, to be transferred to /Interp

18-267 "Syntax errors in example codes"

No action this meeting, but will hang onto the paper.

This points out two simple typo errors in examples, but this is not the meeting for typo changes

/INTERP

18-264r1; no action

Recessed for the day at 5:00 PM

2. Tuesday, October 16, 2018 8:00 am

2.1 F202x Plenary (13-010)

Subgroup Heads

/JoR:

** Motion to continue working on 18-238r1 "Minimum-width Character Format" [Nagle] (Nagle/Lionel)
Vote: 6 to continue 2 do not continue rest undecided, and motion carries.

** Motion to continue working on 18-253r1 "line length and/or statement length" [Nagle] (Nagle/Clune)
Motion carried to continue working on this feature.

** Motion to continue working on 18-266r1 "Add reductions to DO CONCURRENT" [Klimowicz]
(Nagle/Klimowicz). Motion carried to continue working on this feature.

** Motion to continue working on 18-268, "Control of leading zero in formatted numeric output"
[Lionel] (Nagle/Lionel)
Motion carried to continue working on this feature.

/Data:

No papers for this morning

/HPC

No papers for this morning

/Interp

No papers for this morning

2.2 Tutorials (if needed)

Craig Rasmussen led a discussion about the Future of Fortran in High-performance scientific applications.

Recessed to subgroup meetings at 9:15

2.3 Subgroup meetings

2.4 Subgroup reports (4:50 pm)

/JoR:

For vote and/or discussion tomorrow:
18-238r2, 18-253r2, 18-257r1, 18-259r1, 18-266r2, 18-272, 18-273

/Data:

For vote tomorrow: 18-274
Status:

18-263 "Construct association in an ASSOCIATE construct"
No further action as section 19.5.1.6 covers it.

18-239 "logical short-circuits"

No further action because superceded by 18-274

18-241 "Persistency option for dummy argument attributes"

no further action, 18-271 is response.

18-244 "Irregularities (associate names)"

no action, no response

18-254 "real-complex interoperability"

no further action, response is 18-275

/HPC:

No papers for tomorrow.

/Interp:

For vote tomorrow:

18-236r1, 18-251r1

Recessed at 5:15.

3. Wednesday, October 17, 2018 8:00 am

3.1 F202x Plenary (13-010)

Subgroup Heads

/JoR:

**** Motion to accept 18-238r2 "Minimum-width Character Format" [Nagle] (Nagle/Lionel)**

Motion amended to change A0 to AT, and G0 does not change. Motion passed as amended with Unanimous Consent (UC)

**** Motion to accept 18-253r2 "line length and/or statement length" [Nagle] (Nagle/Lionel)**

Motion amended to specify default characters and that maximum line length be a minimum of 10K although processor may extend that. Motion passed as amended with UC.

**** Motion to accept 18-257r1 "log<n> and friends" [Nagle] (Nagle/Menard)**

Motion amended to change "LOG" to "LOGICAL", and to say "LOGICAL with storage size of 8 bits". Motion passed as amended with UC

**** Motion to accept 18-259r1 "part 2 functions as amended" [Nagle] (Nagle/Klimowicz)**

Motion amended to put these functions into a new intrinsic module (name to be finalized later). Motion passed as amended with UC.

**** Motion to accept 18-266r2 "Add reductions to DO CONCURRENT" [Klimowicz] (Nagle/Rouson).**

After a lively discussion, the motion was withdrawn for rewrite.

**** Motion to accept 18-272 "Degree trigonometric functions" [Menard] (Nagle/Menard)**
Motion was amended to add 'PI' functions as well as 'degree' functions. Motion passed as amended UC.

**** Motion to accept 18-273 "virtuous procedures" [Nagle] (Nagle/Clune)**
Motion was amended to use "simple" instead of "virtuous", and to allow ERROR STOP in such a routine. Motion passed as amended UC.

/Data:

Discussion: Does subgroup continue working on 18-274 "If-then or else" [Cohen]

Straw Vote to continue:

Yes	No	Undecided
13	0	1

Straw vote to use keywords or special characters:

Keyword	Characters	Undecided
6	5	3

18-254 "real-complex interoperability" no further action, response is 18-275

/HPC:

No papers, no business.

/Interp:

For vote this afternoon: 18-236r1, 18-251r1

Recessed to subgroup 8:45

[3.2 Tutorials \(if needed\)](#)

[3.3 Subgroup meetings](#)

[3.4 Subgroup reports \(4:30 pm\)](#)

/JoR:

For vote tomorrow: 18-258r1, 18-266r3, 18-276, 18-278, 18-279

/Data:

18-262 is being transferred to /Interp

18-271 "Response to 18-241" [Cohen] is an info paper

18-275 "real-complex interoperability" [Cohen] is an info paper

/HPC

For vote tomorrow: 18-277, 18-280, and 18-281

/Interp

For vote tomorrow: 18-262r1

From this morning:

** Motion to accept 18-236r1 "ACOSH result value error/typo F18/0001"

[Shterenlikht, Cohen] (Cohen/Snyder)

Motion was amended to repair a misspelling in the paper and motion passed with UC

** Motion to accept 18-251r1 "Are internal procedures allowed in generic interface blocks F18/0002"

[Steidel](Cohen/Steidel)

Motion passed without amendments, with UC

Recessed until tomorrow at 5:00

4. Thursday, October 18, 2018 8:00 am

4.1 F202x Plenary (13-010)

Subgroup Heads

/JoR

** Motion to accept 18-258r1, "cstring-fstring" [Nagle] (Nagle/Lionel)

After a lively discussion, motion was withdraw for rework in committee

** Motion to accept 18-266r3 "Add reductions to DO CONCURRENT" [Klimowicz] (Nagle/Klimowicz)

Motion amended to use “~zero (ones complement to zero)” to indicate ‘all bits on’, and motion passed as amended with UC

** Motion to accept 18-276 "IEEE pi trigonometric functions" [Menard] (Nagle/Menard)

Motion amended to include ATAN2, and to include more details on the equations supported by the “pi” instructions. Motion passed as amended with UC.

** Motion to accept 18-278, “sign of zero on write” [Corbett] (Nagle/Corbett)

Motion amended to call the specific “zero-sign” and motion passed as amended with UC.

** Motion to accept 18-279 “Extending usability of deferred-length strings” [Clune, Lionel]

(Nagle/Lionel)

After another lively discussion, motion was amended with clarifications. Motion as amended was accepted with unanimous consent.

/Data

18-256r1 will be for vote this afternoon.

/HPC

** Motion to accept 18-277 “Put with notify” [Steidel] (Long/Steidel)

Motion amended to include the alternative that EVENT_TYPE be used instead of defining another intrinsic type (NOTIFY_TYPE). Motion passed as amended with UC.

** Motion to accept 18-280 "Constraint C825" [Filippone/Rouson/Long] (Long/Clune)

Motion amended with slight verbage changes, and motion passed as amended with UC.

** Motion to accept 18-281 "Templates" [Long] (Long/Steidel)

Motion amended to include GENERIC TYPE proposal and to fix a couple of typographical errors. Motion passed as amended with UC.

4.2 Tutorials (if needed)

4.3 Subgroup meetings

4.4 US TAG (4:15 pm)

D. Nagle

4.4.1 Systematic review of INCITS/ISO/IEC JTC 1/SC22 TS 18508:2015: Additional Parallel Features in Fortran

https://standards.incits.org/apps/org/workgroup/pl22.3/download.php/102876/PL22.3-2018-00283_ISO_IEC_18508_2015_Systematic-Review-Ballot-Form-JTC1.docx

The recommended actions are:

- Withdraw
- Revise/Amend
- Confirm
- Abstain due to lack of consensus
- Abstain due to lack of access to national expertise
- Stabilize

Motion to withdraw by Nagle/Menard. Roll call vote below.

Organization	Representative	Vote
Cray Computer, Inc	Bill Long	Y
Berkley Labs	Brian Friesen	Y
Corbett	Bob Corbett	Y
JPL	Van Snyder	Y
Kernelyze	Not present	-
Lionel	Steve Lionel	Y
IBM	Daniel Chen	Y
Intel	Lorri Menard	Y
NCAR	Dan Nagle	Y
NASA	Tom Clune	Y
NVidia	Gary Klimowicz	Y
Oak Ridge Nat. Lab	Not present	-
Sandia National Lab	Damian Rouson	Y

4.4.2 Systematic review of INCITS/ISO/IEC JTC 1/SC22 TS 29113:2012 (vers 2): Further interoperability of Fortran with C

https://standards.incits.org/apps/org/workgroup/pl22.3/download.php/102878/PL22.3-2018-00284_ISO_IEC_29113_2012_Systematic-Review-Ballot-Form-JTC1.docx

The recommended actions are:

- Withdraw
- Revise/Amend
- Confirm
- Abstain due to lack of consensus
- Abstain due to lack of access to national expertise
- Stabilize

Motion to withdraw by Nagle/Menard. Roll call vote below.

Organization	Representative	Vote
Cray Computer, Inc	Bill Long	Y
Berkley Labs	Brian Friesen	Y
Corbett	Bob Corbett	Y
JPL	Van Snyder	Y
Kernelyze	Not present	-
Lionel	Steve Lionel	Y
IBM	Daniel Chen	Y
Intel	Lorri Menard	Y
NCAR	Dan Nagle	Y
NASA	Tom Clune	Y
NVidia	Gary Klimowicz	Y
Oak Ridge Nat. Lab	Not present	-
Sandia National Lab	Damian Rouson	Y

Note: After the meeting, it was discovered that the ballot forms, as voted on, were incorrectly filled in by the secretary. The outcome of the ballots are the same, to withdraw the Technical Specifications as they have been included in the upcoming standard.

The corrected documents are:

https://standards.incits.org/apps/org/workgroup/pl22.3/download.php/102877/PL22.3-2018-00283-001_ISO_IEC_18508_2015_Systematic-Review-Ballot-Form-JTC1.docx

https://standards.incits.org/apps/org/workgroup/pl22.3/download.php/102879/PL22.3-2018-00284-001_ISO_IEC_29113_2012_Systematic-Review-Ballot-Form-JTC1.docx

4.5 Subgroup reports (4:30 pm)

/JoR

For vote tomorrow: 18-258r2

/DATA

18-240r1 "On the semantic options for templates/generics" [Clodius]

No action on paper

18-260 "Generic programming in future Fortran" [DIN]

No action on paper

18-245 "In support of containers" [Snyder]

No action on paper

18-255 "new types from old" [Nagle]

No action on paper

18-265 "Protected components" [Long]

Deferred to m218 (February 2019)

**** Motion to accept 18-256r1 "enums" [Nagle/Cohen] (Cohen/Snyder)**

Motion passed with UC.

/HPC

No papers

/Interp

**** Motion to accept 18-262r1 "Pointer association of component of non-definable selector" F18/0003 [Snyder] (Cohen/Snyder)**

Motion was amended to repair typographical errors, and motion passed with UC.

Recessed for the evening at 4:43PM.

5. Friday, October 19, 2018 8:00 am

5.1 F202x Plenary (13-010)

Subgroup Heads

/JoR

**** Motion to accept 18-258r2, "cstring-fstring" [Nagle] (Nagle/Lionel)**

Motion passed with UC

/DATA

No more business.

/HPC

No more business

/Interp

No more business

5.2 Closing business

5.3 Review of action items (if any)

- Secretary to forward our tag vote (done evening of 10/18)

5.4 Future meetings

D. Nagle

2019 Meeting dates:

Feb 11-15, 2019 m218
Las Vegas, NV USA
Host: Craig Rasmussen

Aug 5-9, 2019 m219
Joint meeting with WG5
Tokyo, Japan

Oct 14-18, 2019 m220
Las Vegas, NV USA
Host: Van Snyder

5.5 Treasurer's report

J. Steidel

15 Oct 2018	Opening balance	\$	2682.36
17 Oct 2018	Meeting fees		700.00

	Subtotal	\$	3382.36
18 Oct 2018	Fairfield Inn	-	0.00

18 Oct 2018	Closing balance	\$	3382.36

Motion to not collect meeting fee at February or June meetings (Steidel/Snyder). Motion passed with Unanimous Consent.

5.6 Closing membership report

L. Menard

During the course of the meeting, Vipul Parekh became an alternate to Steve Lionel.

5.7 Comments from members

Adjournment 8:30

3. Other Business

4. Future Meetings

2019 Meeting dates:

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5. Adjournment

Meeting adjourned 8:30 AM
